

### DMA Channel Jumper Setting

#### NS87332 ECP Mode

	DMA 1 (Default)	DMA 3
JEPF2	1 2 3	1 2 3
JEPF1	1 2 3	1 2 3

### Printer Port Jumper Setting

#### NS87311/312 Printer Port Direction

	OUTPUT (Default)	INPUT	BIDIRECTION
JH	1 2 3	1 2 3	1 2 3
JG	1 2 3	1 2 3	NA

#### NS87332 Printer Port Direction

	OUTPUT
JH	1 2 3
JG	1 2 3

## Memory Subsystem

The 486-PIO-2 is equipped with the memory necessary for running all your applications. Memory comes in the form of DRAM (SMDs) and cache SRAM. This chapter describes these two kinds of memory and gives instructions on how to install each kind on the mainboard.

### Memory Locations

The board layout below shows the locations of the DRAM memory banks and the cache SRAM:

